

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 06-24-03
Application Type:: Regular
Subject Matter:: Utility
Title:: Upper-Bound Calculation for Placed
Circuit Design Performance
Attorney Docket Number:: X-1221 US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 03
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: India
Status:: Full Capacity
Given Name:: Anirban
Family Name:: Rahut
Street:: 1125 Delna Manor Lane #1
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95128

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: India
Status:: Full Capacity
Given Name:: Sudip
Middle Name:: K.
Family Name:: Nag
Street:: 2879 Rubino Circle
City:: San Jose
State or Province:: California

Postal or Zip Code:: 95125

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

R presentative Customer Number::	24309	
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ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124